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APPLICANTS Masanori Tanimura, Kyoto, JAPAN; Torayuki Tsukada, Kyoto, JAPAN; Kousaku Tanaka, Kyoto, JAPAN; ** CONTINUING DATA ***** This application is a 371 of PCT/JP2005/005190 03/23/2005 ** FOREIGN APPLICATIONS ***** JAPAN 2004-086752 03/24/2004 ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/09/2008						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and /KYUNG S LEE/ Acknowledged Examiner's Signature		<input type="checkbox"/> Met after Allowance /KL/ Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 11	TOTAL CLAIMS 12 10	INDEPENDENT CLAIMS 3 2
ADDRESS HAMRE, SCHUMANN, MUELLER & LARSON, P.C. P.O. BOX 2902 MINNEAPOLIS, MN 55402-0902 UNITED STATES						
TITLE Chip Resistor and Manufacturing Method Thereof						
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit			